

**IN THE CLAIMS:**

A. Please cancel claims 1-7 and 23-28 without prejudice or disclaimer.

B. Please amend claims 8-22 as follows:

**Amended Claims With Mark-ups to Show Changes Made**

8. (Amended) A plasma display panel, comprising:

a first substrate;

a plurality of first substrate electrode pairs formed on the first substrate [at constant intervals] in [one] a first direction;

[a first dielectric layer formed on the first substrate including the first substrate electrode pairs;]

a second substrate;

a plurality of second substrate electrodes formed on the second substrate [at constant intervals to cross] in a second direction crossing the first substrate electrode pairs in the first direction;

a [second] dielectric layer formed on the second substrate [including] and the second substrate electrodes;

barriers formed on the [second] dielectric layer [with] aligned between the second substrate electrodes [interposed therebetween];

a phosphor layer formed on the [second] dielectric layer including the barriers; and

a plurality of projections formed on the phosphor layer between the [respective] barriers [at constant intervals in the same direction as the barriers].

9. (Amended) [A] The plasma display panel of claim 8, wherein the plurality of projections are separated from [inner sides of the] each of the plurality of barriers at [their] the lower portion, the upper portion, or the lower and upper portions of each barrier.

10. (Amended) [A] The plasma display panel of claim 8, wherein at least one of the plurality of projections [are gradually narrowed toward their upper portion from their lower portion] is tapered.

11. (Amended) [A] The plasma display panel of claim [10] 8, wherein at least one of the plurality of projections [have] has a conical shape.

12. (Amended) [A] The plasma display panel of claim [10] 8, wherein at least one of the plurality of projections [have] has a polygonal shape.

13. (Amended) [A] The plasma display panel of claim [10] 8, wherein at least one of the plurality of projections [have] has a height equivalent to the height of at least one of the barriers.

14. (Amended) A plasma display panel, comprising:

- a first substrate;
- a plurality of first substrate electrode pairs formed on the first substrate;
- a second substrate;
- second substrate electrodes formed on the second substrate to cross the first substrate electrode pairs;
- a first dielectric layer formed on the second substrate [including] and the second substrate electrodes;
- barriers formed on the first dielectric layer in first and second directions; and
- a second dielectric layer formed on the first substrate [including], wherein the first substrate electrode pairs [at] have a predetermined height, [having] and wherein the second dielectric layer includes a groove of a predetermined width and depth in the first and second directions on a surface region of the second dielectric layer.

15. (Amended) [A] The plasma display panel of claim 14, wherein the barriers [have a lattice shape being crossed in horizontal and vertical directions] in the first direction are perpendicular to the barriers in the second direction, and wherein the groove forms an exhaust path in the second dielectric layer.

16. (Amended) [A] The plasma display panel of claim 14, wherein the groove is formed in a region of the second dielectric layer corresponding to the [first] barriers in the [same] first direction [as the first barriers].

17. (Amended) [A] The plasma display panel of claim 14, wherein the groove is formed in a region of the second dielectric layer corresponding to the [second] barriers in the [same] second direction [as the second barriers].

18. (Amended) [A] The plasma display panel of claim 14, wherein the groove is formed in a region of the second dielectric layer corresponding to the [first] barriers in the [same] first direction [as the first barriers,] and [is formed in a region of the second dielectric layer corresponding to] the [second] barriers in the [same] second direction [as the second barriers].

19. (Amended) [A] The plasma display panel of claim 14, wherein the groove is formed in a corresponding region of the second dielectric layer between the [first] barriers in the [same] first direction [as the first barriers].

20. (Amended) [A] The plasma display panel of claim 14, wherein the groove is formed in a corresponding region of the second dielectric layer between the [second] barriers in the [same] second direction [as the second barriers].

21. (Amended) [A] The plasma display panel of claim 14, wherein the groove is formed in a corresponding region of the second dielectric layer between the [first] barriers in the [same] first direction [as the first barriers,] and [is formed in a corresponding region of the second dielectric layer] between the [second] barriers in the [same] second direction [as the second barriers].

22. (Amended) [A] The plasma display panel of [any one of claims 16 to 18] claim 14, wherein the groove is wider than the [first] barriers [and the second barriers].

**Clean Set of Amended Claims**

8. (Amended) A plasma display panel, comprising:
- a first substrate;
  - a plurality of first substrate electrode pairs formed on the first substrate in a first direction;
  - a second substrate;
  - a plurality of second substrate electrodes formed on the second substrate in a second direction crossing the first substrate electrode pairs in the first direction;
  - a dielectric layer formed on the second substrate and the second substrate electrodes;
  - barriers formed on the dielectric layer aligned between the second substrate electrodes;
  - a phosphor layer formed on the dielectric layer including the barriers; and
  - a plurality of projections formed on the phosphor layer between the barriers .
9. (Amended) The plasma display panel of claim 8, wherein the plurality of projections are separated from each of the plurality of barriers at the lower portion, the upper portion, or the lower and upper portions of each barrier.

10. (Amended) The plasma display panel of claim 8, wherein at least one of the plurality of projections is tapered.

11. (Amended) The plasma display panel of claim 8, wherein at least one of the plurality of projections has a conical shape.

12. (Amended) The plasma display panel of claim 8, wherein at least one of the plurality of projections has a polygonal shape.

13. (Amended) The plasma display panel of claim 8, wherein at least one of the plurality of projections has a height equivalent to the height of at least one of the barriers.

14. (Amended) A plasma display panel, comprising:

- a first substrate;
- a plurality of first substrate electrode pairs formed on the first substrate;
- a second substrate;
- second substrate electrodes formed on the second substrate to cross the first substrate electrode pairs;
- a first dielectric layer formed on the second substrate and the second substrate electrodes;

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barriers formed on the first dielectric layer in first and second directions; and  
a second dielectric layer formed on the first substrate, wherein the first substrate electrode pairs have a predetermined height, and wherein the second dielectric layer includes a groove of a predetermined width and depth in the first and second directions on a surface region of the second dielectric layer.

15. (Amended) The plasma display panel of claim 14, wherein the barriers in the first direction are perpendicular to the barriers in the second direction, and wherein the groove forms an exhaust path in the second dielectric layer.

16. (Amended) The plasma display panel of claim 14, wherein the groove is formed in a region of the second dielectric layer corresponding to the barriers in the first direction.

17. (Amended) The plasma display panel of claim 14, wherein the groove is formed in a region of the second dielectric layer corresponding to the barriers in the second direction.

18. (Amended) The plasma display panel of claim 14, wherein the groove is formed in a region of the second dielectric layer corresponding to the barriers in the first direction and the barriers in the second direction.



19. (Amended) The plasma display panel of claim 14, wherein the groove is formed in a corresponding region of the second dielectric layer between the barriers in the first direction.

20. (Amended) The plasma display panel of claim 14, wherein the groove is formed in a corresponding region of the second dielectric layer between the barriers in the second direction.

21. (Amended) The plasma display panel of claim 14, wherein the groove is formed in a corresponding region of the second dielectric layer between the barriers in the first direction and between the barriers in the second direction.

22. (Amended) The plasma display panel of claim 14, wherein the groove is wider than the barriers.

**C. Please add new claims 29-34 as follows:**

29. (New) A plasma display panel, comprising:

a first substrate;

a plurality of first substrate electrode pairs formed on the first substrate in a first direction;

a first dielectric layer formed on the first substrate and the first substrate electrode pairs;

a second substrate;

a plurality of second substrate electrodes formed on the second substrate in a second direction to cross the first substrate electrode pairs in the first direction;

a second dielectric layer formed on the second substrate and the second substrate electrodes; and

barriers formed on the second dielectric layer, wherein an exhaust path is formed between the first and second dielectric layers.

30. (New) The plasma display panel of claim 29, further comprising:

a phosphor layer formed on the second dielectric layer and the barriers; and

a plurality of projections formed on the phosphor layer between the barriers, wherein the exhaust path is between the barriers, the plurality of projections and the first and second dielectric layers.

31. (New) The plasma display panel of claim 30, wherein the plurality of projections are separated from the barriers by the exhaust path.

32. (New) The plasma display panel of claim 29, wherein the exhaust path comprises a groove formed in the second dielectric layer.

33. (New) The plasma display panel of claim 32, wherein the groove is formed in a discharge space between the barriers forming the exhaust path between the barriers.

34. (New) The plasma display panel of claim 32, wherein the groove is formed above the barriers forming the exhaust path above the barriers.